DEC 0 8 2003 TO TRADEMARKS

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Majid et al.)	Examiner: Parekh, N.	
Application No.: 09/440,595)	Art Unit: 2811	
Filed: 11/15/1999)		
For: MULTIPLE SEMICONDUCTOR CHIP (MULTI-CHIP) MODULE FOR USE IN POWER APPLICATIONS)))		
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Sir:

Introductory Comments

In response to the Final Office Action of September 4, 2003, please amend the aboveidentified patent application as follows:

Serial No.: 09/440,595